



# **SPECIFICATION**

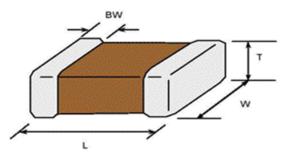
(Reference sheet)

- Supplier : Samsung electro-mechanics
- Product : Multi-layer Ceramic Capacitor
- Samsung P/N : CL02A104MR2NNNC
- Description : CAP, 100nF, 4V, ±20%, X5R, 01005

A. Samsung Part Number

|                                     |   |                        | <br><mark>02</mark><br>② | <u>А</u><br>З | <u>104</u><br>④ | <u>M</u><br>5 | <u>R</u><br>6                   | <mark>2</mark><br>⑦ | <u>N</u><br>8 | <u>N</u><br>9 | <u>N</u><br>10 | <u>C</u><br>11                |      |       |      |  |
|-------------------------------------|---|------------------------|--------------------------|---------------|-----------------|---------------|---------------------------------|---------------------|---------------|---------------|----------------|-------------------------------|------|-------|------|--|
| ① Se<br>② Siz                       | eries<br>ize                                      | Samsung<br>01005 (ii   | •                        | Cera          |                 | •             | citor<br>± 0.0                  | 2                   | mm            |               | W:             | 0.20 ±                        | 0.02 | mm    |      |  |
| <ul><li>4 Ca</li><li>5 Ca</li></ul> | ielectric<br>apacitance<br>apacitance<br>olerance | X5R<br>100 nF<br>±20 % |                          |               |                 | -             | Inner<br>Term<br>Platir<br>Prod | inat<br>ng          |               | е             |                | Ni<br>Cu<br>Sn 100%<br>Normal | 6    | (Pb F | ree) |  |
| _                                   | ated Voltage<br>hickness                          | 4 V<br>0.20 ±          | nm                       |               |                 | 10            | Spec<br>Pack                    | ial                 | g             |               |                | Reserve<br>Cardboa            |      |       |      |  |

## B. Structure and dimension



| Samsung P/N     | Dimension(mm) |           |           |           |  |  |  |  |  |
|-----------------|---------------|-----------|-----------|-----------|--|--|--|--|--|
|                 | L             | W         | Т         | BW        |  |  |  |  |  |
| CL02A104MR2NNNC | 0.40±0.02     | 0.20±0.02 | 0.20±0.02 | 0.10±0.03 |  |  |  |  |  |

#### C. Samsung Reliability Test and Judgement condition

|                   | Performance                            | Test condition  |  |  |  |  |  |
|-------------------|--|---|--|--|--|--|--|
| Capacitance       | Within specified tolerance             | 1 kt $\pm 10\%$ 0.5 $\pm$ 0.1Vrms<br>*A capacitor prior to measuring the capacitance is heat<br>treated at 150°C+0/-10°C for 1 hour and maintained in |  |  |  |  |  |
| Tan δ (DF)        | 0.125 max.                             | ambient air for 24±2 hours.   |  |  |  |  |  |
| Insulation        | 10,000Mohm or 10Mohm·µF                | Rated Voltage 60~120 sec.   |  |  |  |  |  |
| Resistance        | Whichever is smaller                   |   |  |  |  |  |  |
| Appearance        | No abnormal exterior appearance        | Microscope (×20)  |  |  |  |  |  |
| Withstanding      | No dielectric breakdown or             | 250% of the rated voltage   |  |  |  |  |  |
| Voltage           | mechanical breakdown                   |   |  |  |  |  |  |
| Temperature       | X5R                                    |   |  |  |  |  |  |
| Characteristics   | (From -55°C to 85°C, Capacitance chang | ge should be within ±15%)   |  |  |  |  |  |
| Adhesive Strength | No peeling shall be occur on the       | 100g·F, for 10±1 sec.   |  |  |  |  |  |
| of Termination    | terminal electrode                     |   |  |  |  |  |  |
| Bending Strength  | Capacitance change : within ±12.5%     | Bending to the limit (1mm)  |  |  |  |  |  |
|                   |  | with 1.0mm/sec.   |  |  |  |  |  |
| Solderability     | More than 75% of terminal surface      | SnAg3.0Cu0.5 solder   |  |  |  |  |  |
|                   | is to be soldered newly                | 245±5℃, 3±0.3sec.   |  |  |  |  |  |
|                   |  | (preheating : 80~120 $^\circ C$ for 10~30sec.)  |  |  |  |  |  |
| Resistance to     | Capacitance change : within ±7.5%      | Solder pot : 270±5℃, 10±1sec.   |  |  |  |  |  |
| Soldering heat    | Tan δ, IR : initial spec.              |   |  |  |  |  |  |
| Vibration Test    | Capacitance change : within ±5%        | Amplitude : 1.5mm   |  |  |  |  |  |
|                   | Tan δ, IR : initial spec.              | From 10Hz to 55Hz (return : 1min.)  |  |  |  |  |  |
|                   |  | 2hours $\times$ 3 direction (x, y, z)   |  |  |  |  |  |
| Moisture          | Capacitance change : within ±12.5%     | With rated voltage  |  |  |  |  |  |
| Resistance        | Tan δ : 0.25 max                       | 40±2℃, 90~95%RH, 500+12/-0hrs   |  |  |  |  |  |
|                   | IR : 500Mohm or 0.5Mohm $\cdot \mu F$  |   |  |  |  |  |  |
|                   | Whichever is smaller                   |   |  |  |  |  |  |
| High Temperature  | Capacitance change : within ±12.5%     | With 100% of the rated voltage  |  |  |  |  |  |
| Resistance        | Tan δ : 0.25 max                       | Max. operating temperature  |  |  |  |  |  |
|                   | IR : 1,000Mohm or 0.5Mohm · μF         |   |  |  |  |  |  |
|                   | Whichever is smaller                   | 1000+48/-0hrs   |  |  |  |  |  |
| Temperature       | Capacitance change : within ±15%       | 1 cycle condition   |  |  |  |  |  |
| Cycling           | Tan δ, IR : initial spec.              | Min. operating temperature $\rightarrow$ 25 °C  |  |  |  |  |  |
|                   |  | $\rightarrow$ Max. operating temperature $\rightarrow$ 25 °C  |  |  |  |  |  |
|                   |  | 5 cycle test  |  |  |  |  |  |

\* The reliability test condition can be replaced by the corresponding accelerated test condition.

### D. Recommended Soldering method :

Reflow ( Reflow Peak Temperature : 260+0/-5°C, 10sec. Max )

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