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The CENTRAL SEMICONDUCTOR J112 is a silicon N-Channel JFET designed for chopper and switching applications.

MARKING: FULL PART NUMBER

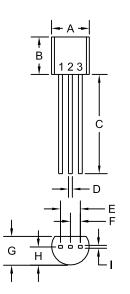
MAXIMUM RATINGS: (T _A =25°C) Drain-Gate Voltage		SYMBOL	35				
		V _{DG}		•			
Gate-Source Voltage		V _{GS}	35	V			
Continuous Gate Current		IG	50	mA			
Power Dissipation		PD	350	mW			
Operating and Storage Junction Temperature		T _J , T _{stg}	-65 to +150	°C			
ELECTRICAL CHARACTERISTICS: (T _A =25°C unless otherwise noted)							
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS			
IGSS	V _{GS} =15V		1.0	nA			
IDSS	V _{DS} =15V	5.0		mA			
BVGSS	I _G =1.0μΑ	35		V			
V _{GS(OFF)}	V _{DS} =5.0V, Ι _D =1.0μΑ	1.0	5.0	V			
^r DS(ON)	V _{DS} =0.1V, V _{GS} =0		50	Ω			
C _{dg(on)}	V _{DS} =V _{GS} =0, f=1.0MHz		28	pF			
C _{sg(on)}	V _{DS} =V _{GS} =0, f=1.0MHz		28	pF			
C _{dg(off)}	V _{GS} =10V, f=1.0MHz		5.0	pF			
C _{sg(off)}	V _{GS} =10V, f=1.0MHz		5.0	pF			

R0 (4-December 2014)



J112

SILICON N-CHANNEL JFET



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A (DIA)	0.175	0.205	4.45	5.21			
В	0.170	0.210	4.32	5.33			
С	0.500	-	12.70	-			
D	0.016	0.022	0.41	0.56			
Е	0.100		2.54				
F	0.050		1.27				
G	0.125	0.165	3.18	4.19			
Н	0.080	0.105	2.03	2.67			
	0.015		0.38				
TO-92 (REV: R1)							

LEAD CODE: 1) Drain

2) Source 3) Gate

3) Gale

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TO-92 CASE - MECHANICAL OUTLINE

R1

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

· Custom bar coding for shipments

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- · Up-screening capabilities

- · Custom product packing
- - · Special wafer diffusions
 - PbSn plating options
 - · Package details
 - · Application notes
 - · Application and design sample kits
 - · Custom product and package development

CONTACT US

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